1. **Course description, goals, objectives, and learning outcomes**

The academic discipline "System Programming" belongs to the mandatory (regulatory) educational components of the education program, namely to the professional training cycle. It has the code PM 7 in the list of components of the educational program.

**Reason and motivations for studying**: the need to understand the principles of programmatic control of the Central Processor Unit (CPU) directly. This discipline will give knowledge of how the interaction of Hardware and Software components is carried out in the Computer System. That is, it combines into a single complex knowledge of the architecture of computer systems and their programming.

**The goal of the "System Programming" course is**: train the basic architecture and programming environment of Intel 64 and IA-32 processors, the data-structure formats for hexadecimal and binary numbers of integer and floating-point values, the instruction set of the processor and the opcode structure, fundamental of low-level programming on Assembler language.

**The purpose of the discipline** is the formation of a number of competencies among students, namely:

**ABILITY**:
- to understand the principles of processor control at the software level;
- to understand the sequence of actions during the development of a program in machine language;
- to analyze the structure of the program in machine language;
- to develop the programs on Assembler language;
- to analyze the processes that are carried out during the compilation and linking of programs;
to use macro-assemblers opportunities in programming;
• to analyze the use of computer system resources by programs, in particular memory;
• also competencies PC 16, PC 17 from the MATRIX 5 of EDUCATIONAL PROFESSIONAL PROGRAM.

After mastering the academic discipline, students must demonstrate such learning outcomes:

**KNOWLEDGE:**
• structures and components of System Software;
• machine language command structures and formats;
• basic storage formats of data according to IEEE 754 -2008;
• x86-64 processor modes;
• Assembler program structures;
• Assembler compiler directives;
• interaction of system programs during their execution;

**SKILL:**
• to determine the sequence of actions for the development of a system program on Assembler language;
• to use integrated development environments (IDE) of system software;
• to analyze messages to the compiler during debugging programs;
• to analyze a program listing in machine language;
• to check the operability of the developed program by using correct control examples;
• to search and correct syntax and logical errors in the system program;

2. Prerequisites and post-requisites of the course (the place of the course in the scheme of studies in accordance with curriculum)

Interdisciplinary Connections: To successfully study the «System Programming» course, students must master the material and have certain knowledge, skills and abilities in such disciplines: «History of Science and Technique», «Computer Discrete Mathematics», «Computer Systems and Networks Fundamentals», «Programming Fundamentals», «Object-Oriented Programming», also have a basic level of English proficiency not lower than A2.

Knowledge and skills acquired during the study of the discipline «System Programming» can be used in the future when mastering the following courses: «Software Engineering Components», «Operating Systems», «Safety of Software», as well as during course and diploma design.

3. Content of the course

Section 1. Basic concepts of the Assembler language.
Topic 1.1. The processor as a programming object.
• The place of the Assembler language in the classification of programming languages.
• The structure of the processor according to the von Neumann concept.
• Chronology processor architectures of INTEL and AMD.
• Principles of organization and modes of use of computer memory.
• x86-64 microprocessor register memory organization.

Topic 1.2. IEEE 754-2008 Standard for number representation.
• Unsigned integers.
• Signed integers.
• Floating-point numbers.
• Binary-decimal number format BCD.

**Topic 1.3. Processor x86 instruction formats.**
• General command structure in machine language.
• The concept of operation code.
• The concept of a command prefix.
• Varieties of addressing in commands.
• Dependence of the command format on the type of addressing.
• Mod/RM field.
• SIB field.
• Assembler command recording formats.
• Assembler command code invariance.

**Section 2. Assembler programming.**
**Topic 2.1. Developing programs in the Assembler language.**
• Stages of program development.
• Compiler operation modes.
• Linker operation modes.
• Using debuggers and disassemblers.
• Assembler program structure.
• Compiler Directives.
• Flags register and main flags.

**Topic 2.2. Using macro-assemblers.**
• Macros and macro declaration.
• Macro variables and macro calculations.

**Topic 2.3. Using Procedures and Subroutines.**
• Stack Memory Principles.
• Internal and external procedures.
• Ways to pass parameters to procedures and return them from a procedure.
• Recursive procedure call.
• Developing Dynamic Link Libraries.

4. **Bibliography**

4.1 Basic:
4.2. Supplementary:


5. Methodology

<table>
<thead>
<tr>
<th>Section and topic titles</th>
<th>Number of hours including</th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Total</td>
<td>Lectures</td>
</tr>
</tbody>
</table>

### Section 1. Basic concepts of the Assembler language

**Topic 1.1. The processor as a programming object.**

- 8 lectures
- 4 practical seminars
- 3 test hours

**Total by section 1:** 63 lectures, 18 practical seminars, 3 test hours, 8 self-study

### Section 2. Assembler programming

**Topic 2.1. Developing programs in the Assembler language.**

- 18 lectures
- 6 practical seminars
- 2 test hours

**Total by section 2:** 69 lectures, 18 practical seminars, 10 test hours, 18 self-study

### Labs

The main tasks of the Labs cycle are the receipt by students of the necessary practical skills in the development of separate modules for controlling computing processes, ensuring the performance of special functions on Assembler and low-level programming.

<table>
<thead>
<tr>
<th>N in/o</th>
<th>Name of laboratory work (computer workshop)</th>
<th>Number of classroom hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Internal representation of Integers and Floating-point numbers in the ix86 processor.</td>
<td>2</td>
</tr>
<tr>
<td>2</td>
<td>Investigation of the structure of COM files.</td>
<td>2</td>
</tr>
<tr>
<td>3</td>
<td>investigation of the structure of EXE files</td>
<td>2</td>
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</table>
6. Self-study

<table>
<thead>
<tr>
<th>N</th>
<th>Names of topics and questions that are submitted for independent study</th>
<th>Number of hours of self-study</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>Performance of tasks on the topic of each Lecture session (see chapter 5) – 1 hour for 1 hour of lecture</td>
<td>36</td>
</tr>
<tr>
<td>2</td>
<td>Preparation for Laboratory Classes (see chapter 5) – 2 hours for 1 hour of Laboratory Work</td>
<td>36</td>
</tr>
<tr>
<td>3</td>
<td>Preparation for Tests (see chapter 6) – 3 hours for each control</td>
<td>6</td>
</tr>
<tr>
<td>4</td>
<td>Exam Preparation</td>
<td>18</td>
</tr>
<tr>
<td>Total</td>
<td></td>
<td>96</td>
</tr>
</tbody>
</table>

7. Course policy

When counting and evaluating laboratory work, the following factors are taken into account:

- Completeness of the task on laboratory work on the individual variant;
- Timeliness of laboratory work according to the schedule;
- Autonomy of laboratory work and no indications of plagiarism;
- Answers to questions on the content of laboratory work during its protection.

When evaluating control works, next consideration is taken into account:

- Correctness and completeness of tasks;
- Number of completed tasks in conditions of limited time;
- Autonomy of tasks and no indications of plagiarism;
- Number of attempts to run controls that precede the one that is estimated.

To prepare for the tests, students receive a list of theoretical questions and the content of typical exercises that will be in the tasks on the test.

At the first and second attestation, the number of laboratory works and tests that were passed at the time of the attestation is taken into account.

8. Monitoring and grading policy


RSA-2 consists of two components:

- starting (Rₜ);
- examination (Rₑ).
Starting points are formed as the sum of points that are obtained as a result of current control activities (laboratory works ($R_L$) and tests ($R_T$)), incentive ($R_I$) and penalty ($R_P$) points:

$$R_S = R_L + R_T + R_I + R_P,$$

where $R_L$ for 8 laboratory works is $8 \times 5 = 40$ points,

$R_T$ for 2 tests is $2 \times 15 = 30$ points.

Thus, the maximum amount of base sum of starting points is $40 + 30 = 70$ points.

Behind the main assessment scale there are incentive and penalty points, which are taken into account in the total amount of points, but are not included in the main RSA scale.

Incentive points take into account the answers to questions and the performance of tasks in lecture classes, the quality of the notes.

Penalty points are provided for late performance of laboratory work, that is, with a delay relative to the schedule.

Thus exam scores are a maximum of 30 points, but for admission to the exam, the student must have at least 60% of the maximum sum of starting points, which is $70 \times 0.6 = 42$ points.

After passing the exam, the starting points $R_S$ are summed up with exam points $R_E$. Assessment of learning outcomes is carried out on a 100-point scale with further conversion of grades into a university scale in accordance with the table:

<table>
<thead>
<tr>
<th>Score</th>
<th>Grade</th>
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<tbody>
<tr>
<td>100-95</td>
<td>Excellent</td>
</tr>
<tr>
<td>94-85</td>
<td>Very good</td>
</tr>
<tr>
<td>84-75</td>
<td>Good</td>
</tr>
<tr>
<td>74-65</td>
<td>Satisfactory</td>
</tr>
<tr>
<td>64-60</td>
<td>Sufficient</td>
</tr>
<tr>
<td>Below 60</td>
<td>Fail</td>
</tr>
<tr>
<td>Course requirements are not met</td>
<td>Not Graded</td>
</tr>
</tbody>
</table>

**Syllabus of the course**

*Is designed by teacher* PhD, Associate Professor, Valerii Pavlov

*Adopted by Department of Computing Technics* (protocol #9, May 10, 2021)

*Approved by the Faculty Board of Methodology* (protocol #10, June 14, 2021)